



## UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE  
 United States Patent and Trademark Office  
 Address: COMMISSIONER FOR PATENTS  
 P.O. Box 1450  
 Alexandria, Virginia 22313-1450  
 www.uspto.gov



Bib Data Sheet

CONFIRMATION NO. 6249

SERIAL NUMBER 10/613,989	FILING DATE 07/08/2003  RULE	CLASS 140	GROUP ART UNIT 3725	ATTORNEY DOCKET NO. 402698
-----------------------------	---------------------------------------	--------------	------------------------	----------------------------------

## APPLICANTS

Hidetaka Yamasaki, Hyogo, JAPAN;

Itaru Matsuo, Tokyo, JAPAN;

Hidekazu Manabe, Tokyo, JAPAN; Kenji Imamura, Tokyo, JAPAN;

Kenichirou Katou, Hyogo, JAPAN;

Mitsutaka Matsuo, Hyogo, JAPAN;

\*\* CONTINUING DATA \*\*\*\*\*

\*\* FOREIGN APPLICATIONS \*\*\*\*\*

JAPAN 2003-1242 01/07/2003

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

\*\* 10/02/2003

Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY JAPAN	SHEETS DRAWING 10	TOTAL CLAIMS 13	INDEPENDENT CLAIMS 4
35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after				
Verified and Acknowledged Examiner's Signature: Initials:				

## ADDRESS

23548

LEYDIG VOIT &amp; MAYER, LTD

700 THIRTEENTH ST. NW

SUITE 300

WASHINGTON, DC

20005-3960

## TITLE

Method of Forming Leads of a Semiconductor Device

☐ All Fees☐ 1.16 Fees ( Filing )